EA W130W-6X9HLW

INCL CONTROLLER ST7567S

Dimension 58.2x44.7x3.9mm

FEATURES

INCL. CONTROLLER ST7567S

VDD = 3.0 ... 3.6V

LOW POWER, PERFECT FOR HAND HELD APPLICATIONS / MAX. 1.5mA@3.3V

OUTLINE DIMENSION 65x43mm ONLY

SINGLE SUPPLY: VOLTAGE GENERATOR ON CHIP

INTEGRATED TEMPERATURE COMPENSATION

CONTRAST ADJUSTMENT BY SOFTWARE

DISPLAY RAM ONBOARD

SERIAL SPI INTERFACE (SI, SCL, A0) OR PARALLEL (DB0 ... DB7)

SUPPORTS Z80- AND 6800- MODES

0.5mm FFC CABLE FOR PLUG-IN AND RELEASE

LED BACKLIGHT WHITE 36~60mA

OPERATING TEMPERATURE RANGE -20..+70°C

ORDERING CODES

MINI GRAPHIC 128X64, ST7567 WITH FFC CABLE, WHITE LED	**EA W130W-6X9HLW**
ZIFF CONNECTOR (SMD), BOTTOM CONTACT	**EA WF050-18S**

ELECTRONIC ASSEMBLY GmbH
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Issue 06.2023

CONTENT

1. PRECAUTIONS IN USE OF LCD MODULES

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3)Don't disassemble the LCM.

(4)Don't operate it above the absolute maximum rating.

(5)Don't drop, bend or twist LCM.

(6)Soldering: only to the I/O terminals.

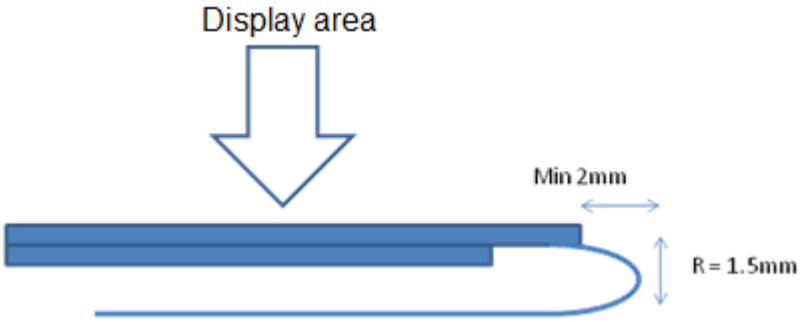
(7)Storage: please storage in anti-static electricity container and clean environment.

(8) DISPLAY VISIONS have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors,capacitors and other passive components will have different appearance and color caused by the different supplier.)

(9)DISPLAY VISIONS have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, DISPLAY VISIONS have the right to modify the version.)

(10) To ensure the stability of the display screen, please apply screen saver after showing 30 mins of fixed display content.

(11) The limitation of FPC bending



(12)Please heat up a little the tape sticking on the components when removing it; otherwise the components might be damaged.

2. GENERAL SPECIFICATION

Item	Dimension	Unit
Number of dots	128 x 64	—
Module dimension	58.2 x 44.7 x 3.9(MAX)	mm
View area	52.0 x 33.5	mm
Active area	47.76 x 30.29	mm
Dot size	0.40 x 0.35	mm
Dot pitch	0.42 x 0.37	mm
LCD type	FSTN Positive Transflective (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/65 duty;1/9 bias	
View direction	6 o'clock	
Backlight Type	LED, White	
IC	ST7567S	
Interface	6800 series/8080 series/4-Line SPI	

3. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_I	-0.3	—	V_{DD}+0.3	V
Digital Power Supply Voltage	V_{DD}-Vss	-0.3	—	4.0	V
LCD Power supply voltage	V0-XV0	-0.3	—	14.0	V

4. ELECTRICAL CHARACTERISTICS

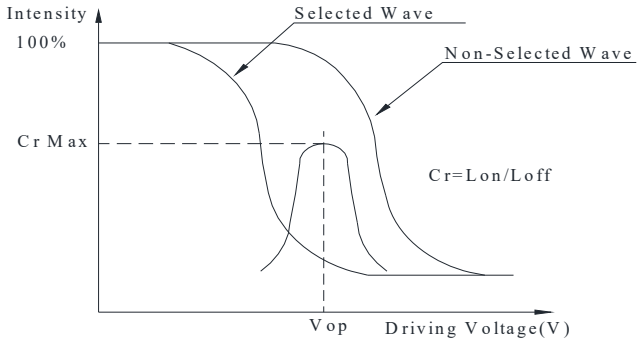
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply Voltage For Logic	V_{DD}-V_{SS}	–	3.0	3.3	3.6	V
Supply Voltage For LCD	V_{OP}	Ta=-20℃	–	–	–	V
		Ta=25℃	8.3	8.5	8.7	V
		Ta=70℃	–	–	–	V
Input High Volt.	V_{IH}	–	0.7V_{DD}	–	V_{DD}	V
Input Low Volt.	V_{IL}	–	V_{SS}	–	0.3V_{DD}	V
Output High Volt.	V_{OH}	–	0.8V_{DD}	–	V_{DD}	V
Output Low Volt.	V_{OL}	–	V_{DD}	–	0.2V_{DD}	V
Supply Current	I_{DD}	V_{DD}=3.3V	–	–	1.5	mA

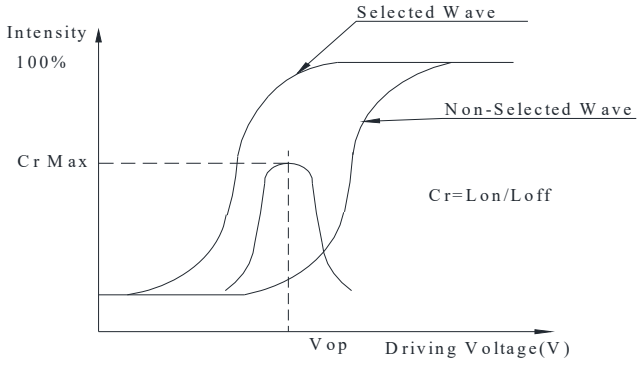
Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance.

5. OPTICAL CHARACTERISTICS

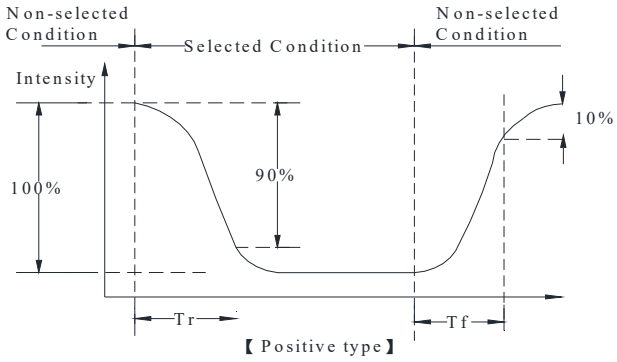
Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	θ	CR≧2	0	—	30	$\phi = 180^\circ$
	θ	CR≧2	0	—	60	$\phi = 0^\circ$
	θ	CR≧2	0	—	45	$\phi = 90^\circ$
	θ	CR≧2	0	—	45	$\phi = 270^\circ$
Contrast Ratio	CR	—	—	5	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	250	350	ms

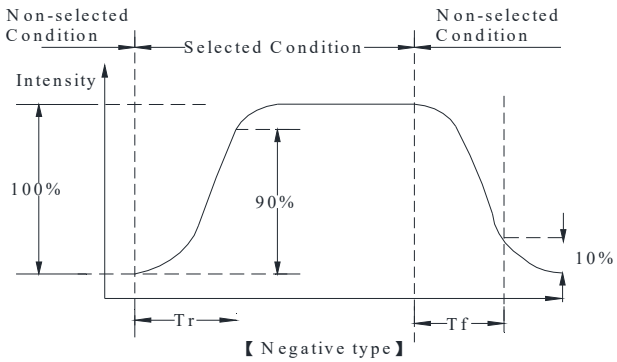
Definition of Operation Voltage (Vop)





Definition of Response Time (Tr , Tf)



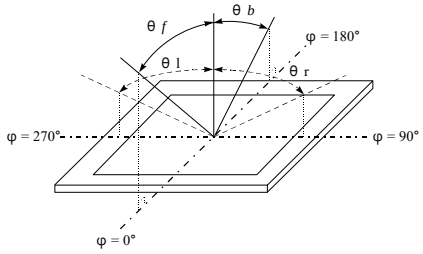


Conditions :

Operating Voltage : Vop — Viewing Angle(θ , ϕ) : 0° , 0°

Frame Frequency : 64 HZ — Driving Waveform : 1/N duty , 1/a bias

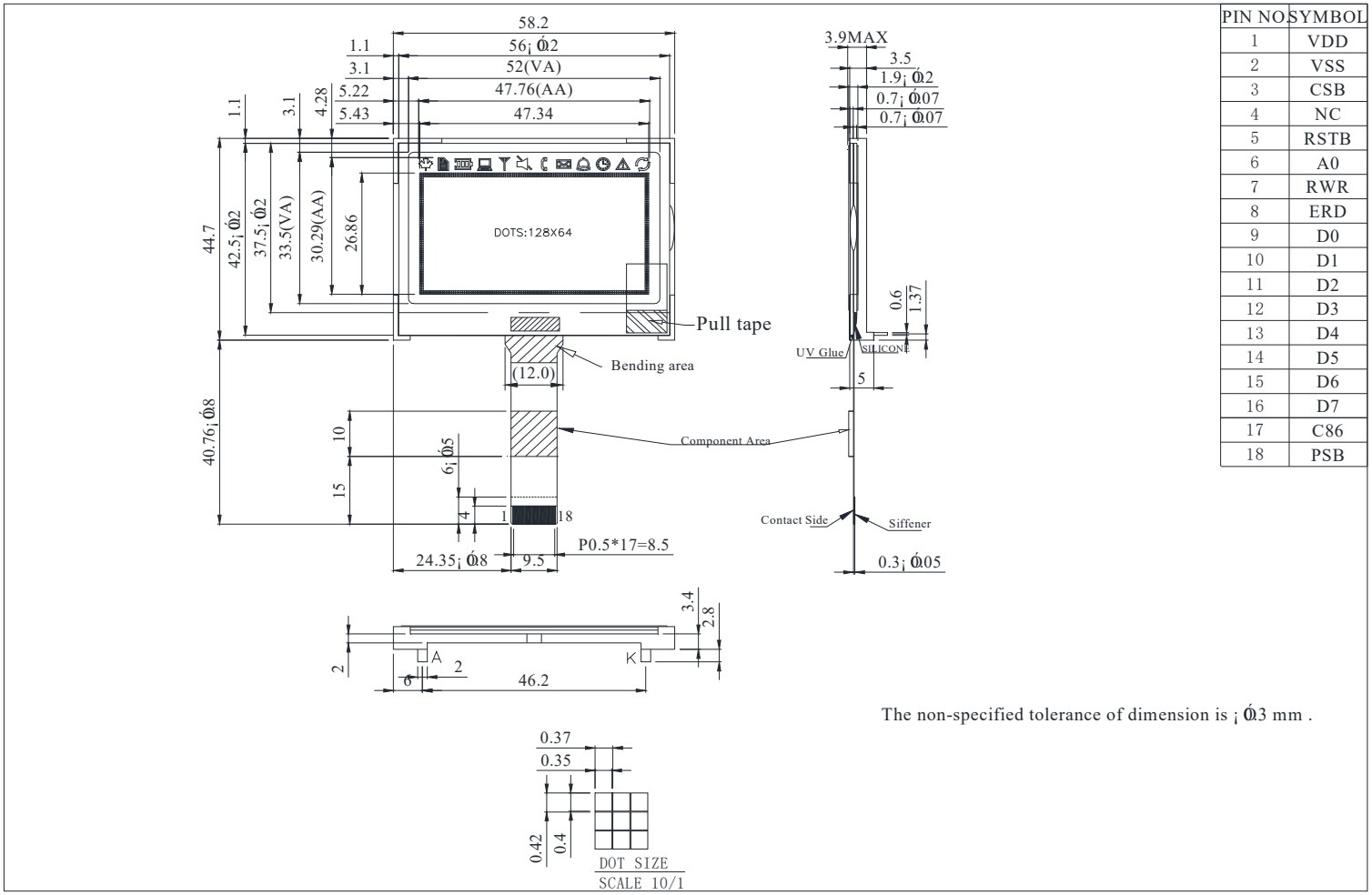
Definition of viewing angle(CR≧2)



6. INTERFACE PIN FUNCTION

<table>
<tr><th>Pin No.</th><th>Symbol</th><th>Description</th></tr>
<tr><td>1</td><td>VDD</td><td>Power supply pin for logic.</td></tr>
<tr><td>2</td><td>VSS</td><td>Ground pin, connected to 0V</td></tr>
<tr><td>3</td><td>CSB</td><td>Chip select input pin.</td></tr>
<tr><td>4</td><td>NC</td><td>NC</td></tr>
<tr><td>5</td><td>RSTB</td><td>Hardware reset input pin</td></tr>
<tr><td>6</td><td>A0</td><td>It determines whether the access is related to data or command.</td></tr>
<tr><td>7</td><td>RWR</td><td>
<table>
<tr><th>C86</th><th>MPU Type</th><th>RWR</th><th>Description</th></tr>
<tr><td>H</td><td>6800 series</td><td>R/W</td><td>Read/Write control input pin.
R/W=“H”: read.
R/W=“L”: write.</td></tr>
<tr><td>L</td><td>8080 series</td><td>/WR</td><td>Write enable input pin.
Signals on D[7:0] will be latched at the rising edge of /WR signal.</td></tr>
</table>
</td></tr>
<tr><td>8</td><td>ERD</td><td>
<table>
<tr><th>C86</th><th>MPU Type</th><th>ERD</th><th>Description</th></tr>
<tr><td>H</td><td>6800 series</td><td>E</td><td>Read/Write control input pin.
R/W=”H“: When E is “H”, D[7:0] are in output mode.
R/W=”L“: Signals on D[7:0] are latched at the falling edge of E signal.</td></tr>
<tr><td>L</td><td>8080 series</td><td>/RD</td><td>Read enable input pin.
When /RD is “L”, D[7:0] are in output mode.</td></tr>
</table>
</td></tr>
<tr><td>9~16</td><td>D0~D7</td><td>8-bit bi-directional data bus. Connect to the data bus of 8-bit microprocessor. When CSB is non-active (CSB=“H”), D[7:0] pins are high impedance.</td></tr>
<tr><td>17</td><td>C86</td><td>C86 selects the microprocessor type in parallel interface mode</td></tr>
<tr><td>18</td><td>PSB</td><td>Interface selection</td></tr>
</table>

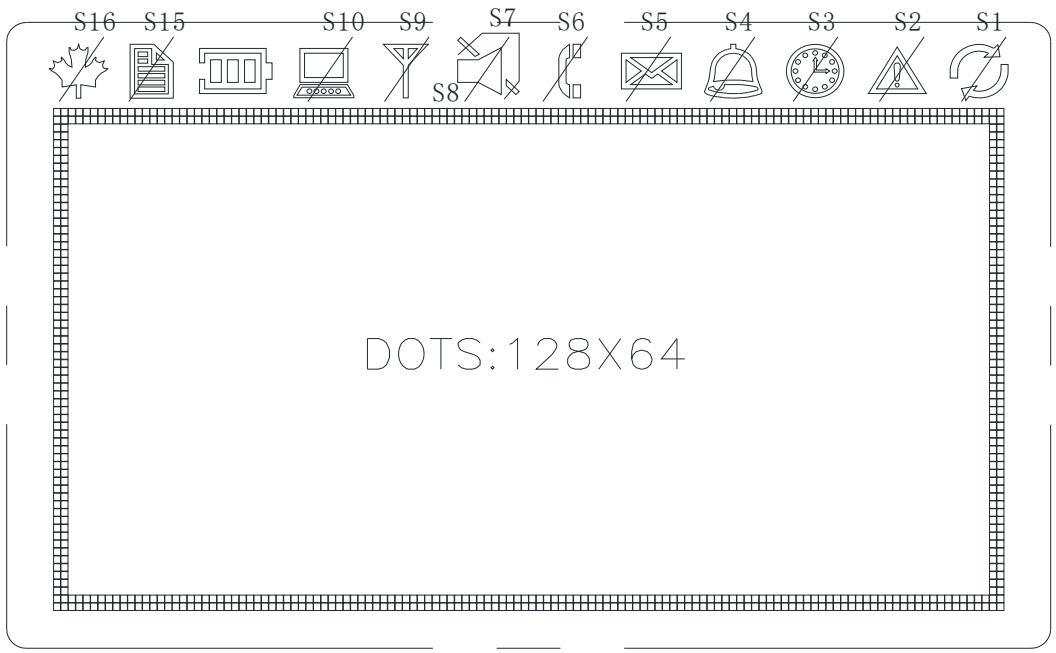
7. CONTOUR DRAWING

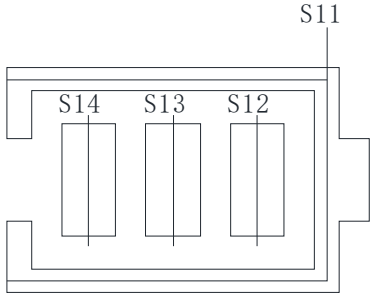


PIN NO.	SYMBOL
1	VDD
2	VSS
3	CSB
4	NC
5	RSTB
6	A0
7	RWR
8	ERD
9	D0
10	D1
11	D2
12	D3
13	D4
14	D5
15	D6
16	D7
17	C86
18	PSB

The non-specified tolerance of dimension is ¡ Ó3 mm .

EA W130W-6X9HLW





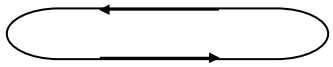
DOT SIZE
SCALE 10/1

	SEG4	SEG14	SEG25	SEG36	SEG47	SEG57	SEG66	SEG69	SEG80	SEG95	SEG107	SEG108	SEG109	SEG110	SEG117	SEG124
COMS	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15	S16

The non-specified tolerance of dimension is ¡ Ó3 mm .

8. RELIABILITY

Content of Reliability Test (Wide temperature, -20℃~70℃)

Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity storage	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20℃ 25℃ 70℃  30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

9. BACKLIGHT INFORMATION

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	36	48	60	mA	V= 3.5V
Supply Voltage	V	—	3.5	—	V	—
Reverse Voltage	VR	—	—	5	V	—
Color coordinate	X	0.25	0.28	0.31	—	ILED=48mA
	Y	0.27	0.30	0.33		
Luminance (Without LCD)	IV	688	860	—	CD/M^2	ILED=48mA
LED Life Time	—	30K	—	—	Hr.	ILED=48mA 25℃,50-60%RH, (Note 2)
Color	White					

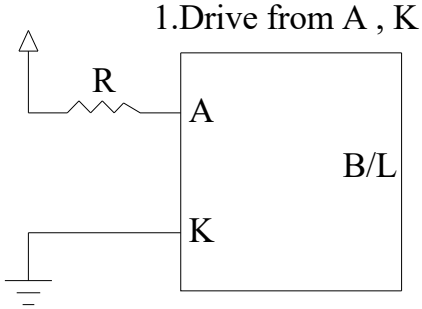
Note: The LED of B/L is drive by current only, drive voltage is for reference only. drive voltage can make driving current under safety area (current between minimum and maximum).

Note 1: Supply current minimum value is only for reference since LED brightness efficiency keeps enhancing. Current consumption becomes less and less to achieve the same luminance.

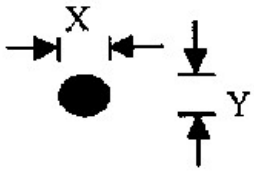
Note2:30K hours is only an estimate for reference.

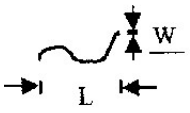
LED B\L Drive Method

1.Drive from A , K



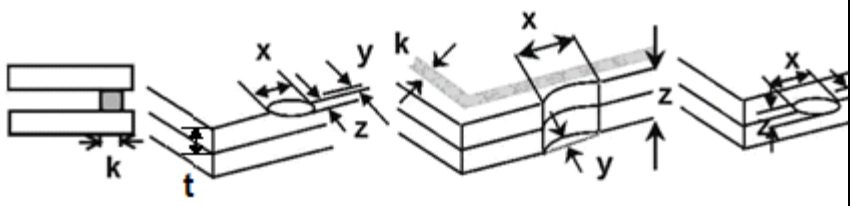
10. INSPECTION SPECIFICATION

<table>
<tr><th>No</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>01</td><td>Electrical Testing</td><td>Missing vertical, horizontal segment, segment contrast defect.
Missing character , dot or icon.
Display malfunction.
No function or no display.
Current consumption exceeds product specifications.
LCD viewing angle defect.
Mixed product types.
Contrast defect.</td><td>0.65</td></tr>
<tr><td>02</td><td>Black or white spots on LCD (display only)</td><td>2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present.
2.2 Densely spaced: No more than two spots or lines within 3mm</td><td>2.5</td></tr>
<tr><td rowspan="2">03</td><td rowspan="2">LCD black spots, white spots, contamination (non-display)</td><td>3.1 Round type : As following drawing
Φ=(x + y) / 2
 

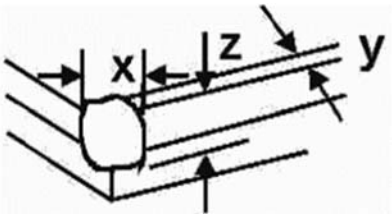
<table>
<tr><th>Size</th><th>Acceptable QTY</th></tr>
<tr><td>Φ≦0.10</td><td>Accept no dense</td></tr>
<tr><td>0.10＜Φ≦0.20</td><td>2</td></tr>
<tr><td>0.20＜Φ≦0.25</td><td>1</td></tr>
<tr><td>0.25＜Φ</td><td>0</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>3.2 Line type : (As following drawing)
 

<table>
<tr><th>Length</th><th>Width</th><th>Acceptable Q TY</th></tr>
<tr><td>---</td><td>W≦0.02</td><td>Accept no dense</td></tr>
<tr><td>L≦3.0</td><td>0.02＜W≦0.03</td><td rowspan="2">2</td></tr>
<tr><td>L≦2.5</td><td>0.03＜W≦0.05</td></tr>
<tr><td>---</td><td>0.05＜W</td><td>As round type</td></tr>
</table></td><td>2.5</td></tr>
<tr><td>04</td><td>Polarizer bubbles</td><td>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.
<table>
<tr><th>Size Φ</th><th>Acceptable Q TY</th></tr>
<tr><td>Φ≦0.20</td><td>Accept no dense</td></tr>
<tr><td>0.20＜Φ≦0.50</td><td>3</td></tr>
<tr><td>0.50＜Φ≦1.00</td><td>2</td></tr>
<tr><td>1.00＜Φ</td><td>0</td></tr>
<tr><td>Total Q TY</td><td>3</td></tr>
</table></td><td>2.5</td></tr>
</table>

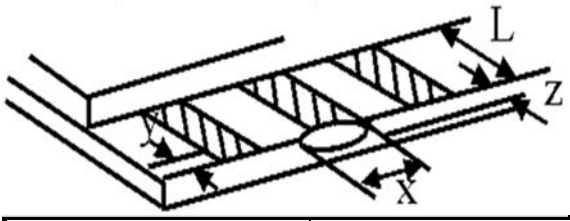
<table>
<tr><th>No</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>05</td><td>Scratches</td><td>Follow NO.3 LCD black spots, white spots, contamination</td><td></td></tr>
<tr><td>06</td><td>Chipped glass</td><td>Symbols Define:
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Glass thickness a: LCD side length
L: Electrode pad length:

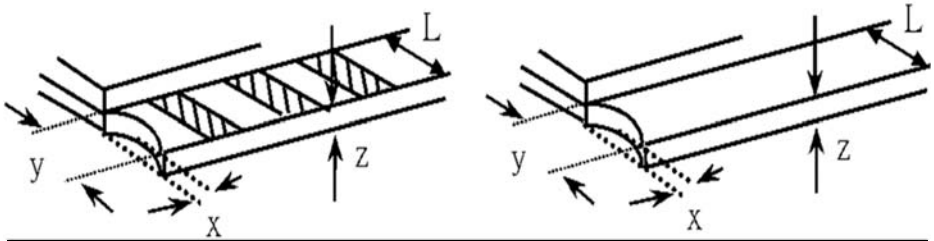
6.1 General glass chip :
6.1.1 Chip on panel surface and crack between panels:
 

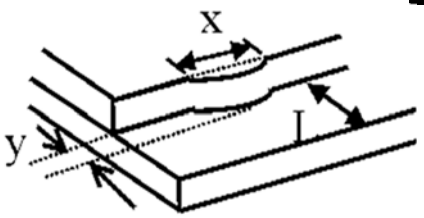
<table>
<tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr>
<tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr>
<tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr>
</table>
⊙If there are 2 or more chips, x is total length of each chip.

6.1.2 Corner crack:
 

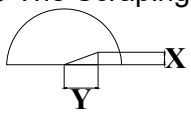
<table>
<tr><th>z: Chip thickness</th><th>y: Chip width</th><th>x: Chip length</th></tr>
<tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr>
<tr><td>1/2t＜z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr>
</table>
⊙If there are 2 or more chips, x is the total length of each chip.</td><td>2.5</td></tr>
</table>

<table>
<tr><th>No</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>06</td><td>Glass crack</td><td>
Symbols :
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Glass thickness a: LCD side length
L: Electrode pad length
6.2 Protrusion over terminal :
6.2.1 Chip on electrode pad :


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦0.5mm</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
6.2.2 Non-conductive portion:


<table>
<tr><th>y: Chip width</th><th>x: Chip length</th><th>z: Chip thickness</th></tr>
<tr><td>y≦ L</td><td>x≦1/8a</td><td>0 ＜ z ≦ t</td></tr>
</table>
⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.
⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.
6.2.3 Substrate protuberance and internal crack.
<table>
<tr><th>y: width</th><th>x: length</th></tr>
<tr><td>y≦1/3L</td><td>x ≦ a</td></tr>
</table>


</td><td>2.5</td></tr>
</table>

No	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  X * Y<=2mm^2	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65
		12.12 Visual defect outside of VA is not considered to be rejection.	0.65

11. MATERIAL LIST OF COMPONENTS FOR ROHS

1. DISPLAY VISIONS Display Co., Ltd hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A：The Harmful Material List

Material	Cd	Pb	Hg	Cr6+	PBB	PBDE	DEHP	BBP	DBP	DIBP
Limited Value	**100 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**	**1000 ppm**
Above limited value is set up according to RoHS.										

2.Process for RoHS requirement：**(only for RoHS inspection)**

(1) Use the Sn/Ag/Cu soldering surface；the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp.：

Reflow：250℃,30 seconds Max.；

Connector soldering wave or hand soldering：320℃, 10 seconds max.

(3) Temp. curve of reflow, max. Temp.：235±5℃；

Recommended customer's soldering temp. of connector：280℃, 3 seconds.

12. RECOMMENDABLE STORAGE

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module.